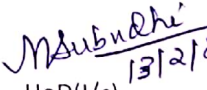


Lesson Plan for the Session Summer-2023
(4th SEMESTER CSE)

DISCIPLINE: Computer Science & Engineering	SEMESTER: 4TH	NAME OF THE TEACHING FACULTY: MR PRAMOD KUMAR SWAIN
SUBJECT: Operating System	NO. OF DAYS/PER WEEK CLASS ALLOTTED: 4	SEMESTER DURATION: 14/02/2023 TO DATE: 23/05/2023 NO.OF WEEKS : 15
WEEK	CLASSDAY	THEORY/PRACTICALTOPICS
1 ST	1 ST	Objectives and Explain functions of operating system.
	2 ND	Evolution of Operating system
	3 RD	Structure of operating system
	4 TH	PROCESSMANAGEMENT Process concept
2 ND	1 ST	process control, interacting processes
	2 ND	inter process messages.
	3 RD	Implementation issues of Processes.
	4 TH	Process scheduling
3 RD	1 ST	Job scheduling
	2 ND	Process synchronization
	3 RD	semaphore
	4 TH	Principle of concurrency
4 TH	1 ST	types of scheduling
	2 ND	MEMORY MANAGEMENT Memory allocation Techniques
	3 RD	Contiguous memory allocation
	4 TH	non contiguous memory allocation
5 TH	1 ST	Swapping
	2 ND	Paging
	3 RD	Segmentation,
	4 TH	virtual memory using paging,
6 TH	1 ST	Demand paging
	2 ND	page fault handling
	3 RD	DEVICEMANAGEMENT
	4 TH	Techniques for Device Management
7 TH	1 ST	Dedicated
	2 ND	shared ,virtual
	3 RD	Device allocation considerations I/O traffic control
	4 TH	I/O Schedule
8 TH	1 ST	I/O Device handlers.
	2 ND	SPOOLING
	3 RD	DEADLOCKS
	4 TH	Concept of deadlock


9 TH	1 ST	Types of deadlock
	2 ND	Dead Lock Detection
	3 RD	Resources allocation Graph
	4 TH	Methods of Deadlock handling
10 TH	1 ST	Recovery & Prevention
	2 ND	Explain Bankers Algorithm
	3 RD	Safety Algorithm
	4 TH	FILEMANAGEMENT
11 TH	1 ST	File organization
	2 ND	Directory & file structure
	3 RD	sharing of files
	4 TH	File access methods
12 TH	1 ST	file systems
	2 ND	reliability
	3 RD	Allocation of disk space
	4 TH	File protection
13 TH	1 ST	secondary storage management
	2 ND	SYSTEMPROGRAMMING
	3 RD	Concept of system programming
	4 TH	and show difference from Application Compiler
14 TH	1 ST	Compiler
	2 ND	Concept of compiler
	3 RD	functions of compiler
	4 TH	functions of interpreter
15 TH	1 ST	Compare compiler and interpreter
	2 ND	Seven phases of compiler
	3 RD	Seven phases of compiler
	4 TH	brief description of each phase

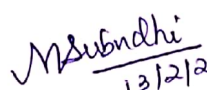

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DISCIPLINE: Computer Science & Engineering	SEMESTER: 4TH	NAME OF THE TEACHING FACULTY: MRS YOGESWARI MAGAR
SUBJECT: Data Communication and Computer Network	NO. OF DAYS/PER WEEK CLASS ALLOTTED: 4	SEMESTER DURATION: 14/02/2023 TO DATE: 23/05/2023 NO.OF WEEKS : 15
WEEK	CLASSDAY	THEORY/PRACTICALTOPICS
1 ST	1 ST	Network
	2 ND	Protocol
	3 RD	Data Communication
	4 TH	Architecture
2 ND	1 ST	Standards
	2 ND	OSI
	3 RD	TCP/IP
	4 TH	Data Transmission &Media
3 RD	1 ST	Data transmission Concepts and Terminology
	2 ND	Analog and Digital Data transmission
	3 RD	Transmission impairments
	4 TH	Channel capacity
4 TH	1 ST	Transmission media
	2 ND	Guided Transmission
	3 RD	Wireless Transmission
	4 TH	Data Encoding
5 TH	1 ST	Concept of Data Encoding
	2 ND	Digital data digital signals
	3 RD	Digital data analog signals
	4 TH	Analog data digital signals
6 TH	1 ST	Analog data analog signals
	2 ND	Data Communication & Data link control
	3 RD	Asynchronous and Synchronous Transmission
	4 TH	Error Detection
7 TH	1 ST	Line configuration
	2 ND	Flow Control
	3 RD	Error Control
	4 TH	Multiplexing
8 TH	1 ST	FDM synchronous TDM
	2 ND	Statistical TDM
	3 RD	Switching &Routing
	4 TH	Circuit Switching networks
9 TH	1 ST	Packet Switching principles
	2 ND	X.25

	3 RD	Routing in Packet switching
	4 TH	Congestion
10 TH	1 ST	Effects of congestion
	2 ND	Congestion control
	3 RD	Congestion control
	4 TH	Traffic Management
11 TH	1 ST	Congestion Control in Packet Switching Network
	2 ND	LAN Technology
	3 RD	Topology
	4 TH	Transmission Media
12 TH	1 ST	LAN protocol architecture
	2 ND	Medium Access control
	3 RD	Medium Access control
	4 TH	Bridges, Hub, Switch
13 TH	1 ST	Ethernet (CSMA/CD),
	2 ND	Ethernet (CSMA/CD),
	3 RD	CSMA/CA
	4 TH	Fiber Channel
14 TH	1 ST	Wireless LAN Technology
	2 ND	TCP/IP
	3 RD	TCP/IP Protocol Suite
	4 TH	Basic Protocol functions
15 TH	1 ST	Principles of Inter networking
	2 ND	Principles of Inter networking
	3 RD	Internet Protocol operations
	4 TH	Internet Protocol


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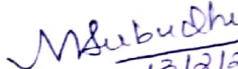

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DISCIPLINE: Computer Science & Engineering	SEMESTER: 4TH	NAME OF THE TEACHING FACULTY: MRS LIPIKA SANDHA
SUBJECT: MICROPROCESSOR & MICROCONTROLLER	NO. OF DAYS/PER WEEK CLASS ALLOTTED: 4	SEMESTER DURATION: 14/02/2023 TO DATE: 23/05/2023 NO.OF WEEKS : 15
WEEK	CLASSDAY	THEORY/PRACTICALTOPICS
1 ST	1 ST	Microprocessor
	2 ND	Introduction to Microprocessor and Microcomputer & distinguish between them.
	3 RD	Concept of Address bus, data bus, control bus & System Bus
	4 TH	General Bus structure Block diagram.
2 ND	1 ST	Basic Architecture of 8085 (8 bit)Microprocessor
	2 ND	Basic Architecture of 8085 (8 bit)Microprocessor
	3 RD	Signal Description (Pin diagram) of 8085Microprocessor
	4 TH	Signal Description (Pin diagram) of 8085Microprocessor
3 RD	1 ST	Register Organizations, Distinguish between SPR & GPR, Timing & Control Module,)
	2 ND	Interrupts:-8085 Interrupts, Masking of Interrupt(SIM,RIM
	3 RD	Interrupts:-8085 Interrupts, Masking of Interrupt(SIM,RIM
	4 TH	Addressing data & Differentiate between one-byte, two-byte &three-byte instructions with examples
4 TH	1 ST	Addressing data & Differentiate between one-byte, two-byte &three-byte instructions with examples
	2 ND	Addressing data & Differentiate between one-byte, two-byte &three-byte instructions with examples
	3 RD	Addressing modes in instructions with suitable examples
	4 TH	Addressing modes in instructions with suitable examples
5 TH	1 ST	Instruction Set of 8085(Data Transfer, Arithmetic, Logical, Branching, Stack& I/O , Machine Control)
	2 ND	Instruction Set of 8085(Data Transfer, Arithmetic, Logical, Branching, Stack& I/O , Machine Control)
	3 RD	Simple Assembly Language Programming of8085 Simple Addition &Subtraction
	4 TH	Logic Operations (AND, OR, Complement 1's & 2's) & Masking of
6 TH	1 ST	Counters & Time delay (Single Register, Register Pair, More than Two Register)
	2 ND	Looping, Counting & Indexing (Call/JMP etc).
	3 RD	Stack &Subroutines programs, Code conversion, BCD Arithmetic & 16 Bit data Operation, Block Transfer.
7 TH	1 ST	Memory & I/O Addressing
	2 ND	TIMINGDIAGRAMS

	3 RD	Define op code, operand, T-State, Fetch cycle, Machine Cycle, Instruction cycle & discuss the concept of timing diagram.
	4 TH	Draw timing diagram for memory read, memory write, I/O read, I/O write machine cycle.
8 TH	1 ST	Draw a neat sketch for the timing diagram for 8085 instruction (MOV, MVI, LDA instruction).
	2 ND	Draw a neat sketch for the timing diagram for 8085 instruction (MOV, MVI, LDA instruction).
	3 RD	Microprocessor Based System Development Aids Concept of interfacing
	4 TH	Define Mapping & Data transfer mechanisms - Memory mapping & I/O Mapping Concept of Memory Interfacing:- Interfacing EPROM & RAM Memories
9 TH	1 ST	Concept of Address decoding for I/O devices Programmable Peripheral Interface: 8255
	2 ND	ADC & DAC with Interfacing
	3 RD	Draw a neat sketch for the timing diagram for 8085 instruction (MOV, MVI, LDA instruction).
	4 TH	Interfacing Seven Segment Displays
10 TH	1 ST	Generate square waves on all lines of 8255 Design Interface a traffic light control system using 8255.
	2 ND	Microprocessor (Architecture and Programming-16 bit-8086)
	3 RD	Register Organization of 8086 Internal architecture of 8086
	4 TH	Signal Description of 8086 General Bus Operation & Physical Memory Organization
11 TH	1 ST	Minimum Mode & Timings, Maximum Mode & Timings
	2 ND	Interrupts and Interrupt Service Routines, Interrupt Cycle,
	3 RD	Non-Maskable Interrupt, Maskable Interrupt
	4 TH	8086 Instruction Set & Programming: Addressing Modes, Instruction Set, Assembler Directives and Operators
12 TH	1 ST	8086 Instruction Set & Programming: Addressing Modes, Instruction Set, Assembler Directives and Operators
	2 ND	Simple Assembly language programming using 8086 instructions.
	3 RD	Simple Assembly language programming using 8086 instructions.
	4 TH	Distinguish between Microprocessor & Microcontroller 8 bit & 16 bit microcontroller
13 TH	1 ST	CISC & RISC processor Architecture of 8051 Microcontroller
	2 ND	Signal Description of 8051 Microcontrollers Memory Organization-RAM structure, SFR
	4 TH	Simple 8051 Assembly Language Programming Arithmetic & Logic Instructions, JUMP, LOOP, CALL

		Instructions, I/O Port Programming
	2 ND	Simple 8051 Assembly Language Programming Arithmetic & Logic Instructions, JUMP, LOOP, CALL Instructions, I/O Port Programming
	3 RD	Interrupts
	4 TH	Timer & Counters
15 TH	1 ST	Timer & Counters
	2 ND	Serial Communication
	3 RD	Microcontroller Interrupts and Interfacing to 8255
	4 TH	Microcontroller Interrupts and Interfacing to 8255


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DISCIPLINE: Computer Science & Engineering	SEMESTER: 4TH	NAME OF THE TEACHING FACULTY: MRS MOUSUMI SUBUDHI
SUBJECT: DATABASE MANAGEMENT SYSTEM	NO. OF DAYS/PER WEEK CLASS ALLOTTED: 4	SEMESTER DURATION: 14/02/2023 TO DATE: 23/05/2023 NO.OF WEEKS : 15
WEEK	CLASSDAY	THEORY/PRACTICAL TOPICS
1 ST	1 ST	BASIC CONCEPTS OF DBMS Purpose of database Systems
	2 ND	Explain Data abstraction
	3 RD	Data base users
	4 TH	Data definition language
2 ND	1 ST	Data Dictionary
	2 ND	Data independence
	3 RD	Entity relationship models
	4 TH	Entity sets and Relationship sets
3 RD	1 ST	Explain Attributes Mapping constraints
	2 ND	E-R Diagram
	3 RD	E-R Diagram
	4 TH	Relational model
4 TH	1 ST	Hierarchical model
	2 ND	Network model
	3 RD	RELATIONAL DATABASE Introduction
	4 TH	Relational algebra
5 TH	1 ST	Different operators select, project, join , simple Examples
	2 ND	Different operators select, project, join , simple Examples
	3 RD	Different operators select, project, join , simple Examples
	4 TH	NORMALIZATION IN RELATIONAL SYSTEM Introduction
6 TH	1 ST	Functional Dependencies
	2 ND	Functional Dependencies
	3 RD	Lossless join
	4 TH	Importance of normalization
7 TH	1 ST	Compare First second and third normal forms
	2 ND	Compare First second and third normal forms
	3 RD	Compare First second and third normal forms
	4 TH	Explain BCNF
8 TH	1 ST	Explain BCNF
	2 ND	STRUCTURED QUERY LANGUAGE Introduction
	3 RD	Elementary idea of Query language

	4 TH	Queries in SQL
9 TH	1 ST	Queries in SQL
	2 ND	Simple queries to create, update, insert in SQL
	3 RD	Simple queries to create, update, insert in SQL
	4 TH	Simple queries to create, update, insert in SQL
	2 ND	Simple queries to create, update, insert in SQL
	3 RD	TRANSACTION PROCESSING CONCEPTS Introduction
	4 TH	Idea about transaction processing
11 TH	1 ST	Transaction & system concept
	2 ND	Transaction & system concept
	3 RD	Desirable properties of transaction
	4 TH	Desirable properties of transaction
12 TH	1 ST	Schedules and recoverability
	2 ND	Schedules and recoverability
	3 RD	CONCURRENCY CONTROL CONCEPTS
	4 TH	Basic concepts
13 TH	1 ST	Locks, Live Lock, Dead Lock,
	2 ND	Locks, Live Lock, Dead Lock,
	3 RD	Locks, Live Lock, Dead Lock,
	4 TH	Locks, Live Lock, Dead Lock,
14 TH	1 ST	Serializability
	2 ND	SECURITY AND INTEGRITY
	3 RD	Authorization and views
	4 TH	Authorization and views
15 TH	1 ST	Security constraints
	2 ND	Security constraints
	3 RD	Integrity Constraints
	4 TH	Discuss Encryption

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